

	Type	L #	Hits	Search Text	DBS	Time Stamp	Comments	Error Definition
1	BRS	L1	14146	fuse\$2 and (((nitride\$2 oxide\$2 insulat\$3 dielectric) ((etch\$3 adj2 (stop\$2 resist\$5))) with (thick\$4 thin\$4)))	USPAT; US-PPG UB	2002/09/26 17:15		
2	BRS	L2	2	((alignment adj1 mark\$2) with (copper cu)) and 1	USPAT; US-PPG UB	2002/09/26 17:13		
3	BRS	L3	1953	((fuse\$2) with (copper cu)) 3 and (((nitride\$2 oxide\$2 insulat\$3 dielectric) ((etch\$3 adj2 (stop\$2 resist\$5))) with (thick\$4 thin\$4)))	USPAT; US-PPG UB	2002/09/26 17:10		
4	BRS	L4	453	(alignment adj1 marks\$2) and 4	USPAT; US-PPG UB	2002/09/26 17:10		
5	BRS	L5	1	fuse\$2 and (((nitride\$2 oxide\$2 insulat\$3 dielectric) ((etch\$3 adj2 (stop\$2 resist\$5))) and T (thick\$4 thin\$4)))	EPO; JPO; DERWEN T	2002/09/26 17:16		
6	BRS	L6	3046	(alignment adj1 marks\$2) and 6	USPAT; US-PPG UB	2002/09/26 17:24		
7	BRS	L7	0	(alignment adj1 marks\$2) and 6	EPO; JPO; DERWEN T	2002/09/26 17:17		
8	BRS	L8	4	(alignment adj1 marks\$2) and 6		2002/09/26 17:17		

Type	L #	Hits	Search Text	DBS	Time Stamp	Comments	Error Definition
9	BRS L9	95	fuse\$2 and ((nitride\$2 oxide\$2 insulat\$3 dielectric)((etch\$3) adj2 (stop\$2 resist\$5))) and (thick\$4 thin\$4))	EPO	2002/09/26 17:25		
10	BRS L10	1861	fuse\$2 and ((nitride\$2 oxide\$2 insulat\$3 dielectric)((etch\$3) adj2 (stop\$2 resist\$5))) and (thick\$4 thin\$4))	DERWEN	2002/09/26 17:25	T	